
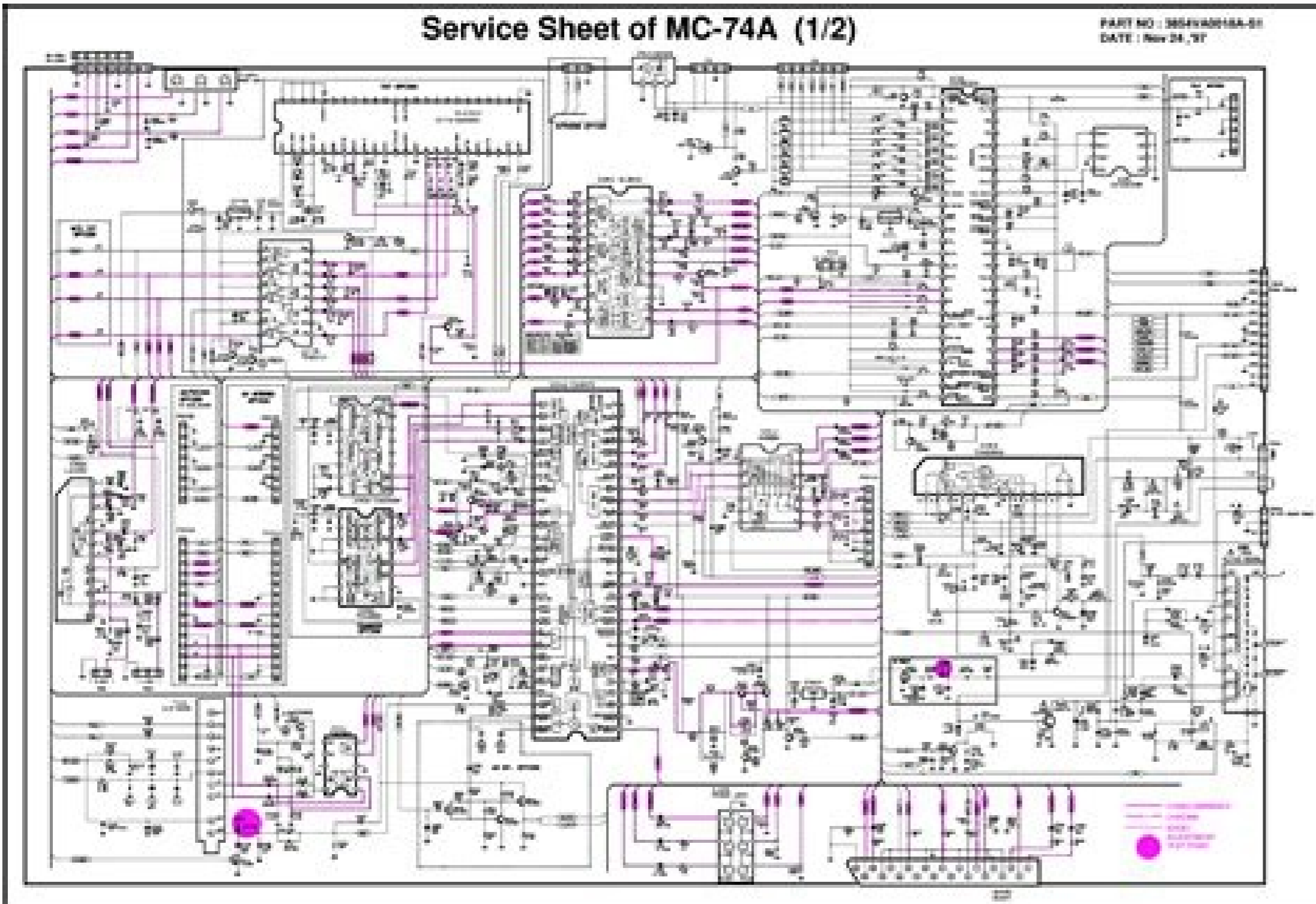
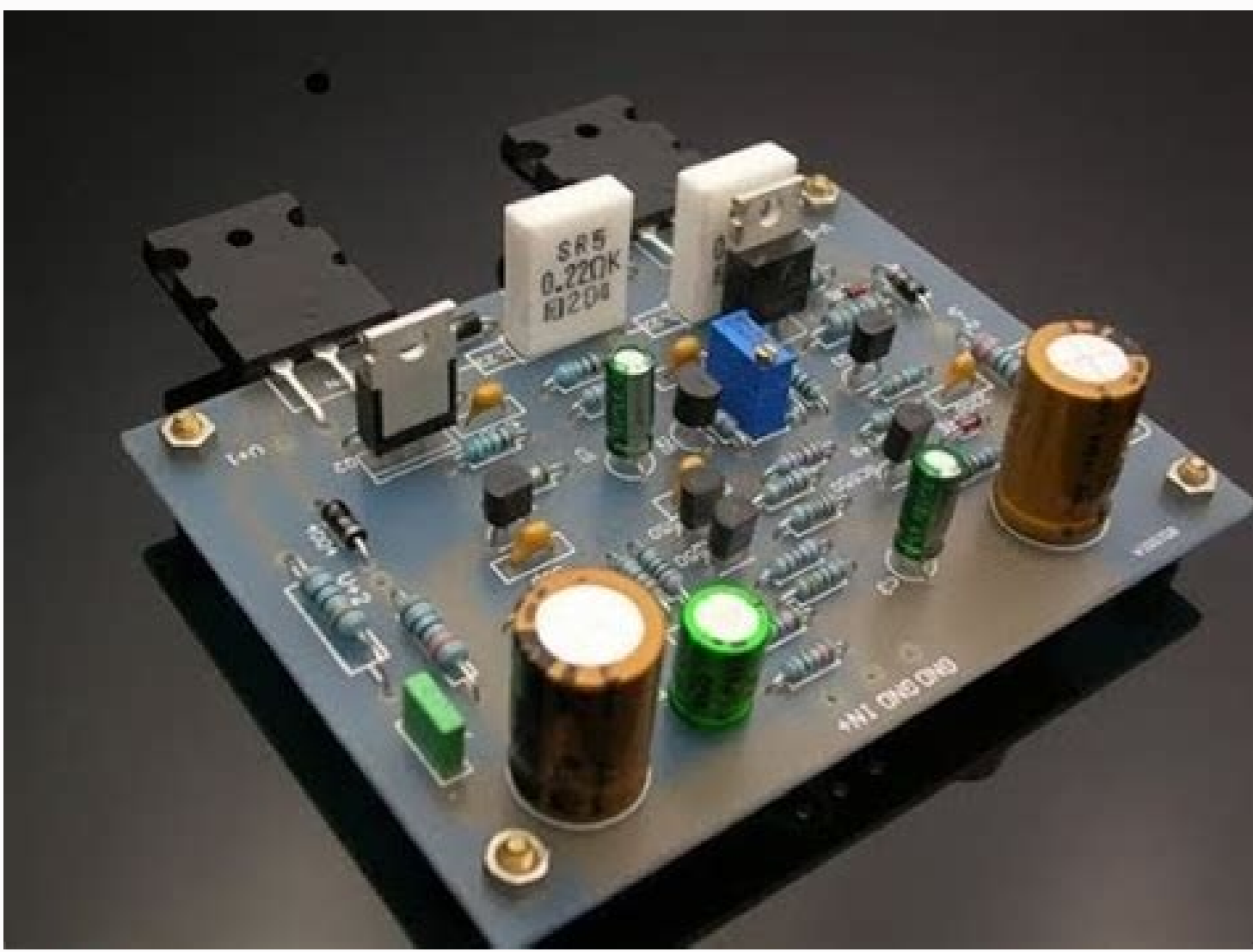
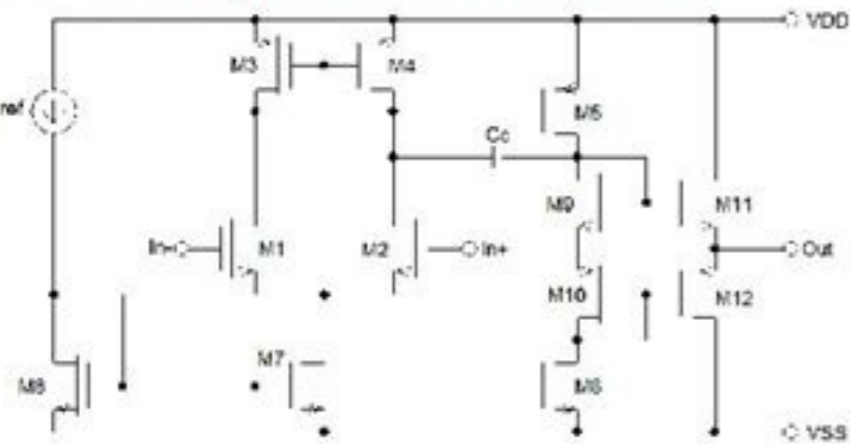


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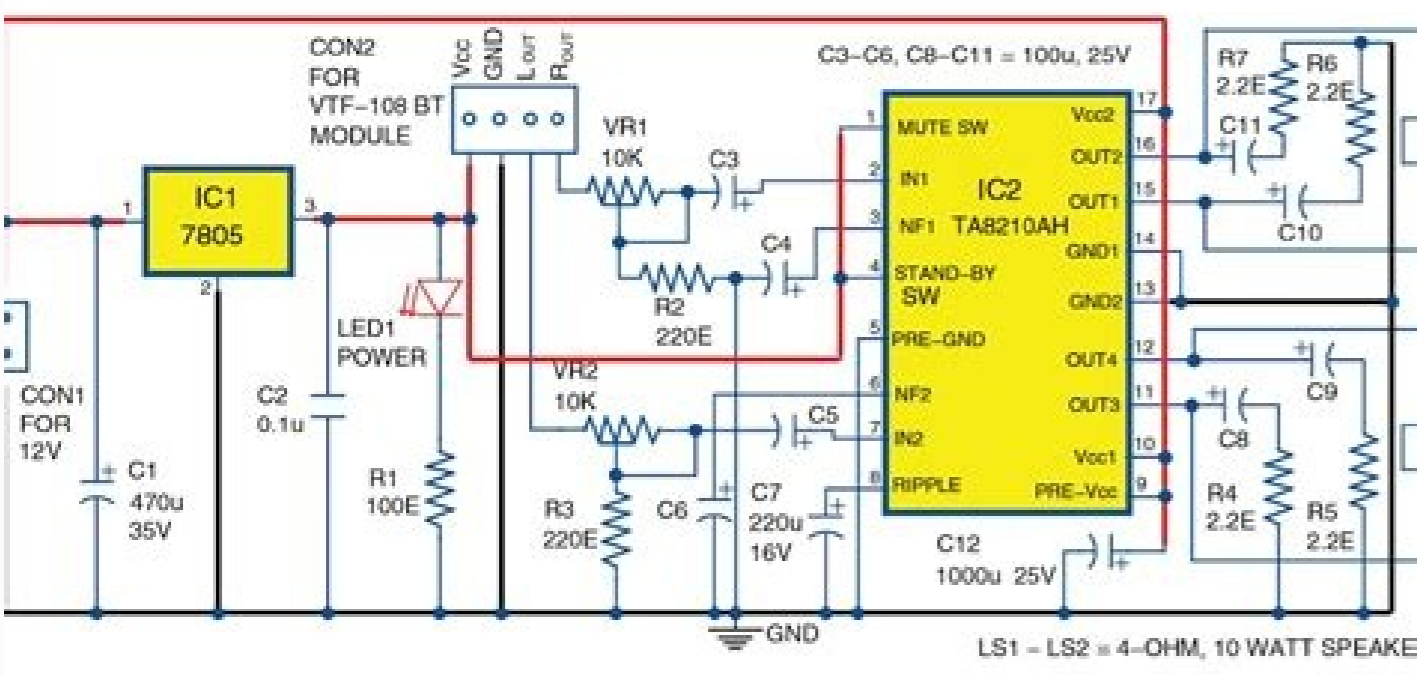


### Basic CMOS Operational Amplifier



The basic CMOS Op Amp configuration consists of two stages. The first stage is a differential amplifier (M1, M2, M7) with current mirror load (M3, M4). It ensures high value of the CMRR. The second stage is a class AB output amplifier with a complementary source-followers (M11, M12). M5 and M6 are common source amplifier with active load. Transistors M9 and M10 set the biasing of the output pair. The couples M3, M7 and M5, M6 are simple current mirrors. Iref is a constant current reference. As we know from previous modules, transistors M1 and M2, and M3 and M4 are identical. Miller capacitance  $C_o$  is the frequency-compensation of the circuit.

CMOS Analog IC  
Sami D. Masarik, TU-SoGa  
Operational amplifiers







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